

L Number	Hits	Search Text	DB	Time stamp
-	13096	(map\$4 with physical nea2r address same logical address) and memory and test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 12:36
-	7204	(map\$4 with physical nea2r address same logical near2 address) and memory and test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 12:47
-	3982	(map\$4 with physical nea2r address same logical near2 address) and memory and (BIST or self-test or self adj test or test) same (control\$4 schedul\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 12:54
-	1102	(map\$4 with physical nea2r address same logical near2 address) and memory and (BIST or self-test or self adj test or test) same (controller scheduler)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 12:57
-	46	(map\$4 with physical near2 address same logical near2 address) and memory and (BIST or self-test or self adj test or test) same (controller scheduler)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 12:59
-	22	(map\$4 with physical near2 address same logical near2 address) and memory and (BIST or self-test or self adj test or test) near6 (controller scheduler)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 13:47
-	9187	(BIST or self-test or self adj test or test) near3 (controller scheduler)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:16
-	6	((BIST or self-test or self adj test or test) near3 (controller scheduler)) same ((map\$4 with physical near4 address) same logical near4 address)) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:31
-	12	((BIST or self-test or self adj test or test) near3 (controller scheduler)) same (physical near4 address same logical near4 address)) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 13:56
-	370	(generat\$3 near4 physical near2 address same logical near2 address) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 13:57
-	23	((controller scheduler) near6 generat\$3 near4 physical near2 address same logical near2 address) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:27
-	9396	(BIST or self-test or self adj test or test) near3 (controller scheduler translator)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:30
-	5	((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 address) same logical near4 address same (row and column))) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:40

-	5	((((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 (address location bit)) same logical near4 (address location bit) same (row and column))) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 14:41
-	0	((((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 address) same logical near4 address same (row and column))) and memory) not (((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 (address location bit)) same logical near4 (address location bit) same (row and column))) and memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:09
-	1096864	physical coordinate "XY"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:10
-	883991	logical or binary or bits	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:10
-	648	(714/733,734).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:11
-	889	(714/733,734).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:12
-	558	(714/710,711).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:12
-	43559	(physical coordinate "XY") with (logical or binary or bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:13
-	69642	(physical coordinate "XY") same (logical or binary or bits)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:19
-	197	(physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:21
-	92	(BIST or built-in near2 self-test or self-test or self adj test) and ((physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:28
-	2	(physical coordinate "XY") and (logical or binary or bits) and ((physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.)) and ((714/710,711).ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:21
-	95	(BIST or built-in near2 self-test or built-in-self-test or self-test or self adj test or in adj circuit adj test) and ((physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/08 15:30

-	1		USPAT; US-PGPUB	2004/04/08 15:59
-	1		USPAT; US-PGPUB	2004/04/08 16:00
-	1		USPAT; US-PGPUB	2004/04/08 16:04
-	1		USPAT; US-PGPUB	2004/04/08 16:09
-	9	(US-6629166-\$ or US-6525987-\$ or US-6469945-\$ or US-6236602-\$).did. or (US-20020176310-\$ or US-20010048625-\$ or US-20030167426-\$ or US-20030120985-\$ or US-20030167428-\$).did.	USPAT; US-PGPUB	2004/04/09 13:53
-	7	((US-6629166-\$ or US-6525987-\$ or US-6469945-\$ or US-6236602-\$).did. or (US-20020176310-\$ or US-20010048625-\$ or US-20030167426-\$ or US-20030120985-\$ or US-20030167428-\$).did.) and ((address\$3 near4 memory) same (column and row))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 14:24
-	1225	(retiming timing) near (interface circuit device unit) and ((address\$3 near4 memory) same (column and row))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/09 14:38